

Single PCB View



Summary - Copper Layers

Integr8tor v2015.10-151006

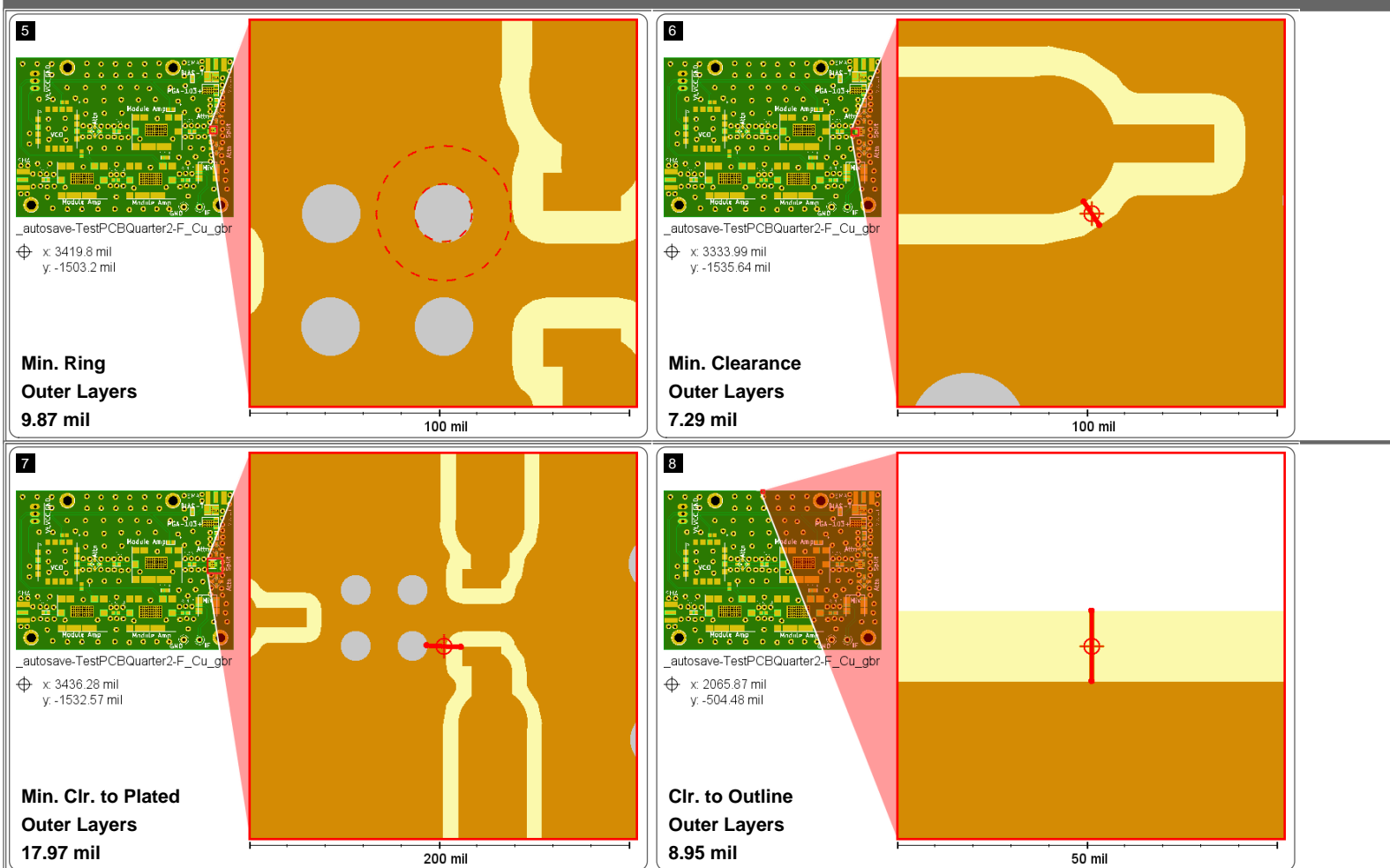
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Name	ieu9rsi9.zip	Id.	1937 - QED With Image Data
Report Generated on	Feb 26, 2016 11:56:29 PM	Customer	InstantDFM
Board Id			

Summary - Sequences

Type	Sequences	Tools	Min. End Dia.	Max. End Dia.	Holes	Min. Ring on Outer	Min. Ring on Inner	Min. Clr. Hole to Copper
			mil	mil		mil	mil	mil
Blind	0							
Buried	0							
PTH	1	7	13.00	126.00	343	9.87		17.97
Plated (Total)	1	7	13.00	126.00	343	9.87		17.97
NPTH	0							
Total	1	7	13.00	126.00	343	9.87		17.97

Summary Minimum Design Characteristics - Locations



Stackup





_autosave-TestPCBQuarter2-F_Cu_g








_autosave-TestPCBQuarter2-B_Cu_g

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


Copper Layers

File	Pos.	Min. Line Width	Min. Ring	Min. Clr. to Copper	Min. Clr. Pad to Pad	Min. Clr. Pad to Track	Min. Clr. Track to Track	Min. Self-spacing	Min. Clr. to PTH	Min. Clr. to NPTH	Min. Clr. to Outline	Copper Area	
		mil	mil	mil	mil	mil	mil	mil	mil	mil	mil	inch ²	%
_autosave-TestPCBQuarter2-F_Cu_gbr	1	> 16.00	9.87	7.29	10.00	7.33	7.29	> 20.00	17.97		8.95	6.2854	95
_autosave-TestPCBQuarter2-B_Cu_gbr	2	> 16.00	10.15	7.33	> 20.00	7.33	> 20.00	> 20.00	18.02		8.95	6.4992	98

Drill Tools

File	Tool Nr.	Span	Type	Method	FilledVia	Countered	End Dia.	Holes (in PCB)	Moves (in PCB)	Double Hits (in File)	Predrill Hits (in File)	Min. Ring on Outer	Min. Ring on Inner	Min. Pad Size
							mil					mil	mil	mil
_autosave-TestPCBQuarter2_drl	1	1-2	PTH	unknown	unknown	unknown	13.00	9	0	0	0	12.30		37.60
_autosave-TestPCBQuarter2_drl	2	1-2	PTH	unknown	unknown	unknown	15.00	4	0	0	0	9.87		34.74
_autosave-TestPCBQuarter2_drl	3	1-2	PTH	unknown	unknown	unknown	20.00	167	0	0	0	12.32		44.64
_autosave-TestPCBQuarter2_drl	4	1-2	PTH	unknown	unknown	unknown	30.00	154	0	0	0	27.65		85.30
_autosave-TestPCBQuarter2_drl	5	1-2	PTH	unknown	unknown	unknown	39.00	3	0	0	0	10.15		59.30
_autosave-TestPCBQuarter2_drl	6	1-2	PTH	unknown	unknown	unknown	43.00	2	0	0	0	17.87		78.74
_autosave-TestPCBQuarter2_drl	7	1-2	PTH	unknown	unknown	unknown	126.00	4	0	0	0	> 32.00		> 190.00

Sequences

Span	Type	Tools	Min. End Dia.	Max. End Dia.	Holes	Min. Ring on Outer	Min. Ring on Inner	Min. Ring on Outer NPTH	Min. Ring on Inner NPTH	Min. Clr. Hole to Copper	Min. Clr. Hole to Outline
			mil	mil		mil	mil	mil	mil	mil	mil
1-2	PTH	7	13.00	126.00	343	9.87				17.97	36.60
All	Plated	7	13.00	126.00	343	9.87				17.97	36.60
All	All	7	13.00	126.00	343	9.87				17.97	36.60

Rout Tools

File	Tool Nr.	Type	Tool Dia.	End Dia.	Draw Length	Nibble Count
			mil	mil	mil	

Routed Holes

File	Hole Nr.	Instances	X Size	Y Size	Draw Length	Nibble Count
			mil	mil	mil	

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Solder Mask

Side	Min. Positive Ring	Min. Negative Ring	Min. Clr. Mask to Mask	Min. Web	Min. Clr. Mask to Copper	Fully Covered Vias	Partly Covered Vias
	mil	mil	mil	mil	mil		
Top	7.87		1.73	0.59	7.26	No	No
Bottom	7.87		3.45	> 10.00	> 10.00	No	No

Files

Initial	Renamed	Format	Function	Position	Color
_autosave-TestPCBQuarter2-F_SilkS.gbr	_autosave-TestPCBQuarter2-F_SilkS_gbr	ger274x	silk	top	white
_autosave-TestPCBQuarter2-F_Mask.gbr	_autosave-TestPCBQuarter2-F_Mask_gbr	ger274x	mask	top	green
_autosave-TestPCBQuarter2-F_Cu.gbr	_autosave-TestPCBQuarter2-F_Cu_gbr	ger274x	outer	1	
_autosave-TestPCBQuarter2-B_Cu.gbr	_autosave-TestPCBQuarter2-B_Cu_gbr	ger274x	outer	2	
_autosave-TestPCBQuarter2-B_Mask.gbr	_autosave-TestPCBQuarter2-B_Mask_gbr	ger274x	mask	bottom	green
_autosave-TestPCBQuarter2-B_SilkS.gbr	_autosave-TestPCBQuarter2-B_SilkS_gbr	ger274x	silk	bottom	white
_autosave-TestPCBQuarter2.drl	_autosave-TestPCBQuarter2_drl	excellon2	plated	1-2	
_autosave-TestPCBQuarter2-Edge_Cuts.gbr	_autosave-TestPCBQuarter2-Edge_Cuts_gbr	ger274x	cad_outline	none	

Comments