# EEC 134 Application Note

# BASEBAND PCB DESIGN AND IMPLEMENTATION

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### **Components Selection**

Before you actually start the PCB design, you should start looking for components and picking them based on their performance. There are many websites where you can order components, such as Mouser Electronics and Digikey. They have a variety of components, and you can place orders easily on OPS (Online Purchasing System, a system by the UC Davis College of Engineering).

In order to minimize the cost of components, we tried to order most components from Mini Circuits E-Z samples. They have over 1000 surface mount free samples.



Figure 1: Mini-circuits E-Z samples page

Also if you are looking for certain characteristics, such as frequency range or package size, you can narrow down the options with the filter below: Make sure the type of the component is actually surface mount for the PCB design.

Menufacturer	Packaping	Series	Frequency Range	Frequency - Center	Tuning Voltage (NDC)	And Harmose, Typ (ditc)	Ric Man	KHOU (MHUV)	Preser (dbm)	Phone Notes Typ (d)CH2	Operating Temperature	Package Case	New Otherselate	maget	Votage Repoly
Crystel Corporation - RPMD	Buh Cutling Tay	CRESCS CRENTS RedBer CVC033 CVC046 CVC046 CVC055 CVC055CAT	18 - 4264c 42 - 4484c 45 - 7044c 45 - 7044c 72 - 7044c 72 - 7044c 73 - 1404c 73 - 1404c 150 - 14044c 151 - 15044c 152 - 17404c 153 - 17404c	40040 440500 440500 5755540 5755540 5755540 5755540 5755540 5755540 5755540 5755640 5755540 1055640 1055640 1055640 1055640	0-50Y 0-52Y 0-62Y 0-59Y 0-29Y 0-29Y 0-3Y 0-5Y 0-5Y 0-5Y	· · · · · · · · · · · · · · · · · · ·	TmA AmA TomA TomA TomA TomA TomA TomA To	104 11 11/7/81 12 12/1/81 13 13 13/7/81 14	45+1 + 45+15    50+1 60+2 60+2 40+1 4+1 4+2 40+3 0+2 25+15 +	-108 -100 -108 -108 -108 -108 -108 -108	40°C - 3'0'C -41°C - 2'0'C -41°C - 40°C -40°C - 40°C -40°C - 40°C -00°C - 30°C -00°C - 30°C -00°C - 30°C -00°C - 30°C -00°C - 30°C	Module Adapter, 1988,F.(1) 6-8805, No Lead (JPN, LCC) 12-070, Valuet 12-060, No Lead 13-060, No Lead 14-080, (13 Pact, No Lead (JPN, LCC) 14-080, No Lead	12. 2016 (s. 12. 2016) (t. 13. 2016) (t. 13. 2017) (t. 13.	5.50mm 0.579 (2.00mm) 0.579 (2.00mm) 0.559 (2.00mm) 0.559 (3.00mm) 0.455 (4.20mm) 0.229 (5.50mm) 0.229 (5.50mm) 0.259 (5.50mm)	2V 27V 3V 33V 45V 55V 40' 84V
Real Lead the Rolls Complet	(feat)	Reat	feat	facet	fase	freed	(And)	And	Read	Anat	freet	Recet	(Area)	(Asot)	(Anist
Parant Add Apply Filler															

Figure 2: Filtering components

Once you click on the component to see more details, you will be able to find a link to the datasheet of the component.

Data Sheet	S-Parameters	and Performa	nce Measurements	Case Style	T&R	PCB Layout	Eval Board *	Environmental Ratings	
Data Sheet	<u>View Data</u> Download Excel File	<u>View Graphs</u>	Download S-Parameters	<u>DQ849</u>	<u>F104</u>	<u>98-PL-328</u>	<u>TB-547-2+</u> <u>Gerber File</u>	ENV08T1	

Figure 2: Finding Datasheet

Datasheets are important during PCB layout design as you will need the dimensions of the pads when you design the footprints of the components. Although some websites do provide already pre-made footprints that you can download, it is advised that you design it yourself as we found some discrepancies and mistakes in the footprints that were on the website. But I will talk about footprints in more details in the next sections.

### **Schematics Design**

During the PCB design process, first we have to decide whether to go with a 2 layer board or 4 layer board. Although 4 layer boards have many flexibilities and functions, our group decided to use a 2 layer board as it is less expensive to fabricate, and for our purposes, 2 layer boards were sufficient enough.

Next question that arises during the PCB design is what software to use. I used Eagle Cad for my PCB designs because it was a free software, and I already had experience using it.

Our group to decided to have 2 separate boards, a baseband board and RF board, and stack the boards on top of each other. We used male and female pin headers to keep them attached and stable.

In order to incorporate components on the PCB, we first need to design footprints. The footprints are the designs of the actual pads that components later will be placed onto. On Eagle, there is library that already had footprints of some common components, like resistors and capacitors. However, in case they are not in the library, you might have to design your own footprints based on the given dimensions from datasheet. Also make sure to pay attention to the pin assignment and carefully follow the order. It is a crucial step. (Look at Figure 3)



Figure 3: Example of a datasheet pin assignment

Once we have footprints ready (as in Figure 4), we can start designing the PCB schematics.



Figure 4: Footprint Example

When we do schematics on Eagle, using pin header is very handy as it is not only helpful for debugging purposes, but also minimizes the long wires between pins. I used label pins for some long wires such as 5V and 3V that need to be connected to many other pins. Instead of having long wires, it looks cleaner and less likely to have wiring mistakes.



Figure 5: Baseband PCB Schematics 1 on Eagle



Figure 6: Baseband PCB schematics 2 on Eagle

I divided the baseband PCB schematics into 2 figures here to see the connections more clearly. But they are in the same schematics file.

## **PCB** Layout Design

When designing a PCB there are a few rules that are recommended to follow

- For 3V and 5V lines, use thicker traces as there will be more current flow
- Try to make angles of traces more than 90 degrees to avoid build ups during the manufacture
- Use many ground vias around the board to make sure the ground planes are connected properly
- For the baseband board, it is recommended to have test pins for later debugging purposes

Later after designing the layout, I printed out my design for the peer review to see if I missed anything.



Figure 7: PCB layout for the baseband PCB

#### **PCB** Fabrication

Once the layout is complete, we can proceed to the fabrication. Our senior design group sent the PCB design to the Bay Area Circuits and Oshpark.

For the Bay Area Circuits, you have to upload Gerber files to the website. Based on the gerber files, the website can detect any possible errors and overlaps and will not let you submit the PCB design. You will receive instant DFM files that shows the locations of the errors. You will have to fix those errors. For instance, if you did not follow the copper to copper minimum width requirements, you will receive a warning as illustrated in the figures 8 and 9. (See Figure 8 and Figure 9)



Figure 8: Instant DFM file

DFM Checks:						
Min. Distance						
1						
13.0 mil						
-						
5.9 mil						
7.0 mil						
16.0 mil						

Figure 9: DFM scheck results

#### **PCB** Assembly

Once we receive the PCB boards, they are ready for soldering. For surface mount components, it is highly advised to use a hot plate to melt solder on the pads. Due to the small sizes of the components (0603), this method is the most efficient and practical.

Here are some tips for using a hot plate:

- It is good to have a magnifying glass to spot even a tiny short circuit
- Try using a temperature no higher than 200 degrees as higher temperatures might damage some components. You can look at each component's datasheet to verify what temperature is safe to use.
- For the baseband board try soldering components in stages. For instance, first do the voltage regulators, then gain stage and then finally the filter. This way it is easier to debug.